UNITED STATES PATENT APPLICATION

of

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for

WAVEGUIDE-TO-SEMICONDUCTOR DEVICE COUPLER

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PRIORITY INFORMATION

This application claims priority from provisional application Ser. No. 60/434,221 filed December 17, 2002, which is incorporated herein by reference in its entirety.

BACKGROUND OF THE INVENTION

The invention relates to the field of waveguide-to-semiconductor coupling devices, and in particular a waveguide-to-semiconductor device having waveguide/semiconductor interface structures with Brewster angles for TM modes and multimode interferometer (MMI) structures at a waveguide end for TE modes.

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The low efficiency in waveguide-to-semiconductor coupling devices is a serious problem in photonics chips. A majority of waveguides are comprised of low index materials (SiNx: 2.2, SiOxNy: 1.5), while other standard semiconductor devices, such as detectors, modulators, emitter, and amplifier are comprised of high index structures (Ge: 4.2, Si:3.5, GaAs: 3.6). One example of inefficient coupling is between a Ge detector and waveguide. Approximately 40% of incident light is reflected between Ge and SiNx causing significant Fresnel loss. In most photonic applications, this amount of loss is unacceptable and very inefficient. In evanescent coupling the propagation velocity mismatch between the waveguide and Ge detector needs a long coupling length. A long coupling length would be an inefficient to be used in photonic applications because size is an essential factor that needs to be controlled for such applications to operate efficiently with minimum loss.

SUMMARY OF THE INVENTION

According to one aspect of the invention, there is provided a waveguide-semiconductor coupling device. The waveguide-semiconductor coupling device includes a waveguide structure that includes a multimode interferometer (MMI) structure so as to minimize the reflections of TE modes in the coupling device. A mesa structure is coupled to the waveguide structure so as to minimize the reflections of TM modes in the coupling device.

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According to another aspect of the invention, there is provided a method of forming a waveguide-semiconductor coupling device. The method includes forming a waveguide structure that includes a multimode interferometer (MMI) structure so as to minimize the reflections of TE modes in the coupling device. Also, the method includes forming a mesa structure that is coupled to the waveguide structure so as to minimize the reflections of TM modes in the coupling device.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic diagram of a waveguide-semiconductor coupling device having a Ge mesa;

- FIGs. 2A-2B are graphs demonstrating the reflections of TE and TM modes of the coupling device described in FIG. 1;
- 20 FIG. 3 is a schematic diagram of a waveguide-semiconductor coupling device using a SiNx waveguide that includes a multimode interferometer (MMI);
 - FIGs. 4A-4D are schematic diagrams illustrating fabrication steps to form the coupling device 20 shown in FIG. 3; and

FIGs. 5A-5B are graphs demonstrating the reflections of TE and TM modes of the coupling described in FIG. 3.

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DETAILED DESCRIPTION OF THE INVENTION

The invention attempts to address the inefficiency presented by waveguide-semiconductor coupling devices by improving its efficiency. By forming an appropriate waveguide/semiconductor interface, one can control the Brewster angles for TM modes which will minimize the reflection of these modes. Moreover, the incorporation of a multimode interferometer (MMI) in the waveguide can be used to minimize the reflection of TE modes in a waveguide-semiconductor coupling device. The combination of the improve waveguide/semiconductor interface and the incorporation of a MMI structure in the waveguide improves significantly over other standard waveguide-semiconductor coupling devices.

FIG. 1 is a schematic diagram of a waveguide-semiconductor coupling device 2 having a Ge mesa 4. The waveguide-semiconductor coupling device 2 includes a SiNx waveguide 6, and a Ge detector 8. The SiNx waveguide 6 includes at its outer most end a polarization rotator 10 to rotate the polarization from TE to TM. Moreover, the SiNx waveguide 6 is coupled with the Ge mesa 4 at a Brewster angle. The Ge mesa 4 is coupled to the Ge detector 8. The SiNx waveguide 6, Ge mesa 4, and Ge detector 8 are all surrounded by SiO₂.

In this arrangement, the Brewster angle is defined as

$$\tan^{-1}\left(n_D/n_{WG}\right)$$
 Eq. 1

where n_D is the index of refraction of the Ge detector and n_{WG} is the index of the waveguide. There is no reflection at the Brewster angle of 27.6° between the SiNx waveguide 6 and the Ge mesa 4 for TM modes. In addition, this arrangement demonstrates reduced reflection for Brewster angles at $27.6\pm10^{\circ}$ for TM modes.

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The Ge mesa 4 is formed using standard techniques in the art. Note that the Ge mesa 4 is formed with a tapered edge 12. This tapered edge 12 provides the means to control TM mode reflections. In addition, the Ge mesa 4 provides an interface between the SiNx waveguide 6 and the Ge detector 8 that efficiently limits the reflections of TM modes. The waveguide 6 is also tapered 14 at its end to form the Brewster angle necessary to limit reflections. The Ge mesa 4 is naturally formed in the selective growth of Ge epilayers on a Si layer 20 at least in UHV-CVD. Afterwards, the waveguide 6 materials are deposited and SiO₂ is deposited to form upper cladding 16 and lower cladding 16 on the formed waveguide 6 and Ge mesa 4.

The dimensions are typically 1 μ m thick Ge mesa 4, 0.2 μ m thick SiNx waveguide 6, 0.1 μ m thick Si layer 20 below the Ge epilayers forming mesa 4, a few μ m thick SiO₂ lower cladding 16, and about 1 μ m thick SiO₂ upper cladding 18. The Ge detector 8 is typically 2 μ m wide and 10 μ m long, and the SiNx waveguide 6 is 1 μ m wide.

To obtain such Brewster angles the coupling between the waveguide 6 and the Ge mesa 4 are formed under various growth facets. In this case, grow facets of {111} and {001} can obtain an angle of approximately of 35°. Also, growth facets {211} and {001} can obtain an angle of approximately 24° and growth facet {001} can obtain an angle of 17.5°. These angles fall in the Brewster angle ± 10°. Note

that these near Brewster angles can be obtained by using other standard processing techniques known in the art.

FIGs. 2A-2B are graphs demonstrating the reflections of TE and TM modes of the coupling device 2 described in FIG. 1. FIG. 2A shows the reflections of the TE modes in the coupling device 2. Note that the coupling device 2 exhibits high TE reflection. This is a problem because the TE reflections are significant even at the Brewster angle. The invention will address this issue more issue hereinafter. FIG. 2B shows the TM reflections in the coupling device 2. In this case, the reflections of TM modes are significantly lower which makes the structure a better coupling device because of this increase in coupling efficiency.

The coupling device 2 has demonstrated a clear promise of providing efficient coupling for TM modes, however, this is not the case for TE modes. The problems also lie with the SiNx waveguide 6 because the index mismatch generates modal mismatch and size mismatch making even poorer coupling efficiency for TE modes.

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FIG. 3 is a schematic diagram of a waveguide-semiconductor coupling device 20 using a SiNx waveguide 22 that includes a multimode interferometer (MMI). In addition, the coupling device 22 includes a Ge mesa 24 and a Ge detector 26 that are formed on a Si layer 28, and is surrounded by SiO₂. The SiNx waveguide 22 also includes a polarization rotator 28 to rotate the polarization from TE to TM.

In forming the waveguide 22, the materials that are used to form a multimode interferometer are selectively incorporated with the materials used to

form the waveguide 22. Afterwards, these materials are deposited on the Ge mesa 24 to form a SINx reversed mesa. In particular, the SiNx reversed mesa is arranged so that a mode is pulled toward the Ge mesa 24 so as to further reduce reflections for TM modes. Moreover, the incorporation of the MMI materials does not degrade TM coupling efficiency.

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FIGs. 4A-4D are schematic diagrams illustrating fabrication steps to form the coupling device 20 shown in FIG. 3. FIG. 4A shows a SOI structure that includes a SiO₂ layer 30 and the Si layer 28. Note that the Si layer 20 should include crystalline Si for efficient growth of the crystalline Ge mesa layer 24. The SOI structure is etched with Si on all areas except those where Ge epilayers will be deposited to form the Ge mesa 24. Afterwards, a layer 32 of SiO₂ is deposited on the side exposed by Si etching. FIG. 4B shows that Ge epilayers are deposited on the Si layer 28 to form the mesa 24. The edge of mesa 24 is facetted with {111}, {311}, or the like planes to form the necessary the Brewster angle described herein. Note the detector is also formed on the Si layer 26. FIG. 4C shows the SiNx materials, which include the MMI structures, being deposited to form waveguide 22. In particular, the waveguide 22 is a SiNx mesa structure having tapered edge in the opposite direction of the Ge mesa 24. FIG. 4D shows a second layer (or upper layer) 36 of SiO₂ being deposited on so the coupling device 20 is enclosed by SiO₂. Furthermore, the polarization rotator 40 can be included at any point after the formation of the waveguide 22.

FIGs. 5A-5B are graphs demonstrating the reflections of TE and TM modes of the coupling 20 described in FIG. 3. FIG. 5A shows the reflections of the TE modes in the coupling device 20. Note that the coupling device 20 exhibits lower TE reflection, as

compared to the coupling device 20 of FIG. 2. Therefore, the incorporation of MMI materials in the SiNx waveguide 22 increases the coupling efficiency of TE mode. FIG. 5B shows the TM reflections in the coupling device 20. In this case, the reflections of TM modes are similar to those shown in FIG. 2B. This illustrates that the incorporation of MMI in the SiNx waveguide 22 does not affect the coupling of TM modes.

The invention provides a coupling device that improves the coupling efficiency of both TE and TM modes. Moreover, the invention can work in any material sets with index mismatch in conjunction with rotators to TM. The flexibility to form such devices allows them to be incorporated in optical chip sets without requiring sufficient difficulty.

Although the present invention has been shown and described with respect to several preferred embodiments thereof, various changes, omissions and additions to the form and detail thereof, may be made therein, without departing from the spirit and scope of the invention.

What is claimed is:

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